



Semiconductor Sector Service Bureau

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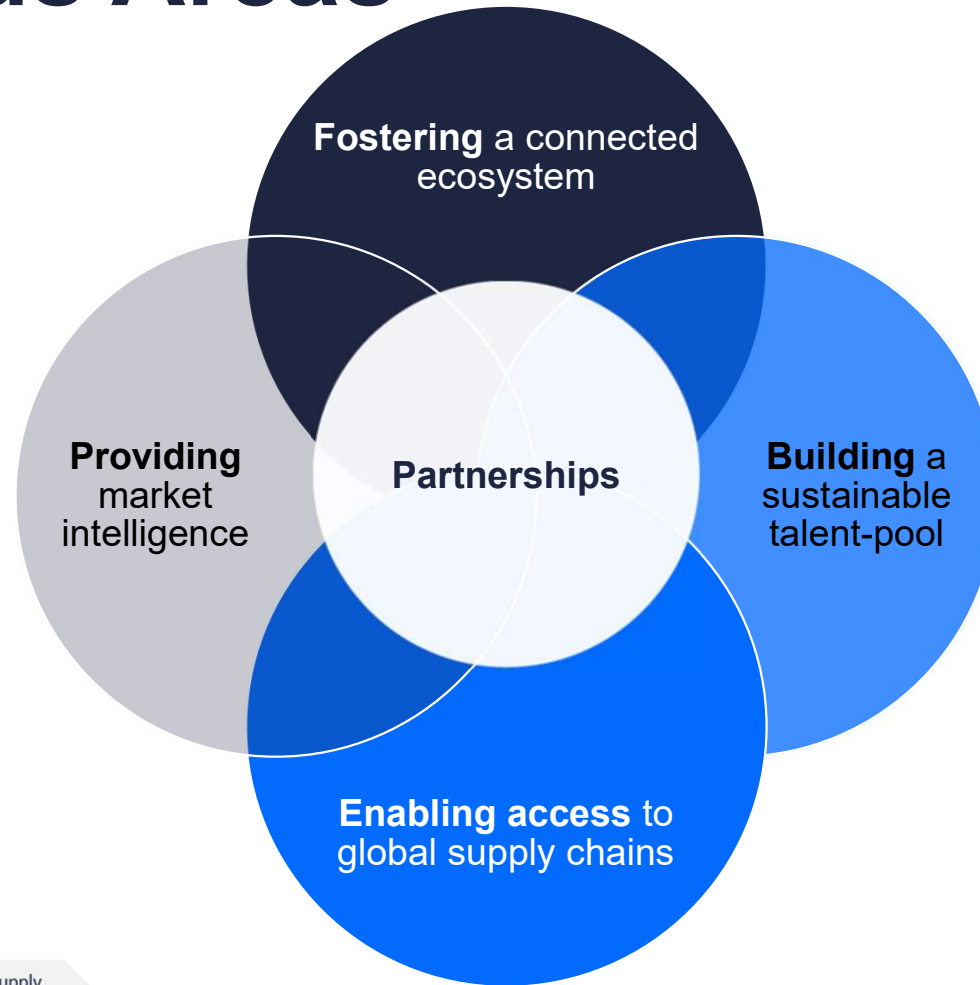
Launched in 2022, the Semiconductor Sector Service Bureau (S3B) is a not-for-profit organisation created to support and grow the semiconductor sector in NSW and Australia across the whole supply chain.

Semiconductor Sector Service Bureau



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S3B Focus Areas



Introduction

Ecosystem

Market Intelligence

Talent Pool

Supply Chain

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Build a dynamic and cohesive semiconductor sector by fostering collaboration and active participation in ecosystem

Connected Ecosystem



Key Goals for Connected Ecosystem

- An **engaged and active semiconductor community** which includes individuals and organisations from government, academic and industry
- **Impactful communications** to inform stakeholders about relevant activity, opportunities and developments.
- **Raise profile of sector** locally and internationally
- Develop **mechanisms for linkages** between industry, researchers and early-stage start-ups



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*Support the growth of the Australian industry
by provision of strategic and functional market
intelligence*

Market Intelligence



Goals of Market Intelligence

- **Promote** the Australian sector, locally and internationally
- Document participants in the ecosystem and **identify their capabilities** and needs
- **Connect organisations** with needs to those that have the capabilities
- Identify opportunities for local **funding, development** and international **partnerships**
- Understand **international approaches** and recommend strategies and tactics for the local ecosystem and supply chain
- Research, document and communicate **industry and market trends**



Identify and address gaps to ensure industry has access to talent required for business to succeed

Talent Pool



Key Goals for Talent Pool

- **Promote careers and training opportunities**
- **Identify and address market demands**, opportunities and skill gaps
- Assist organisations with **short-term talent access**
- NSW NETM Microcredentials
- Digital Microelectronics Design
 - Semiconductor Business Processes, Integrated Circuit Processes and Devices, and Integrated Circuit Packaging
 - Semiconductor Digital Design Verification Concept and Tool Training
 - Semiconductor Digital Implementation Concept and Tool Training



Substantially enhance access to the global semiconductor supply chain by supporting access to competitive fabrication and design tool services

Supply Chain



Key Goals for Supply Chain

- Support access to competitive **fabrication (FAB) and design software (CAD) services**
- Competitive **Electronic Design tool** licences
- **Client Direct** to supply chain services
- Mapping of supply chain services and local requirements
- Surveying and reporting needs of users
- Building FAB/CAD partnerships





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